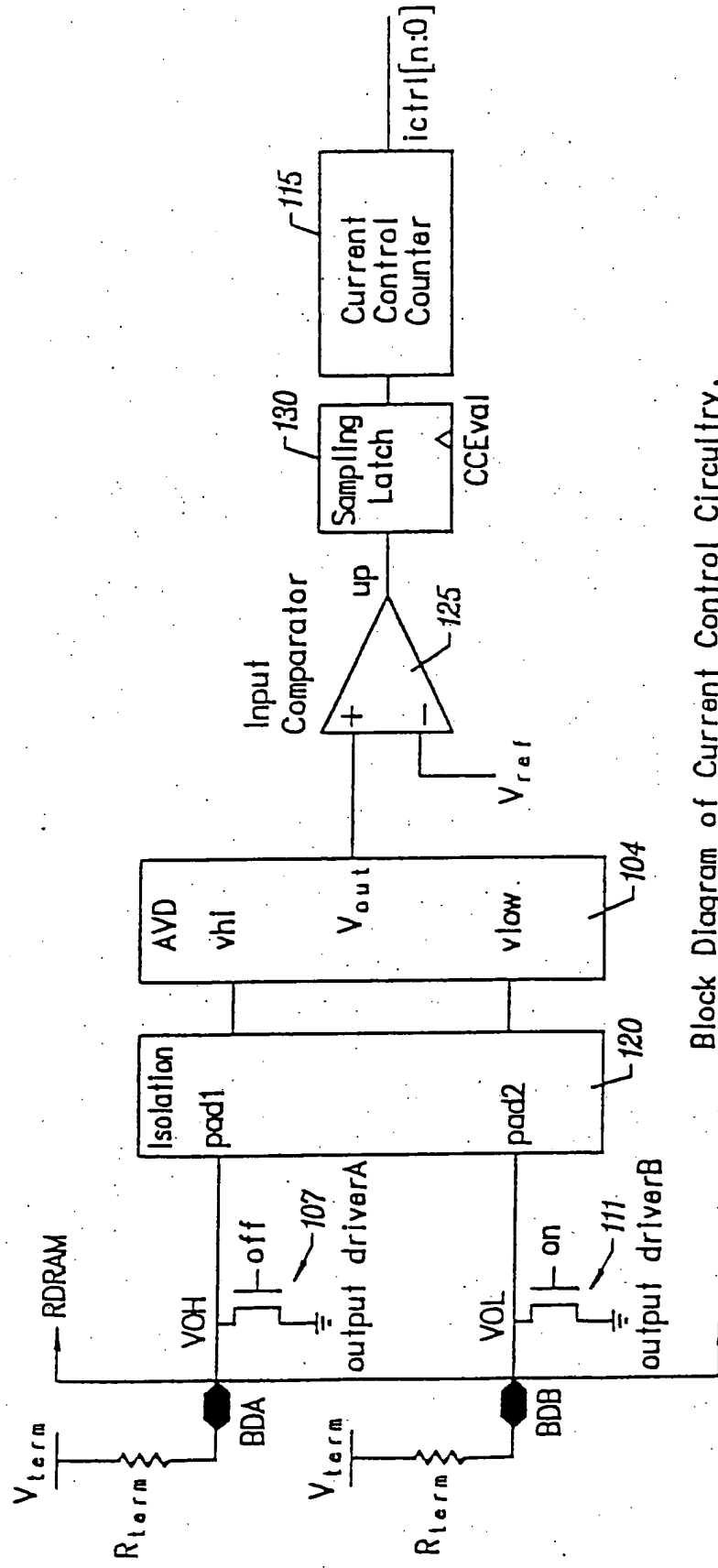


FIG. 1
PRIOR ART



Block Diagram of Current Control Circuitry.

FIG. 2

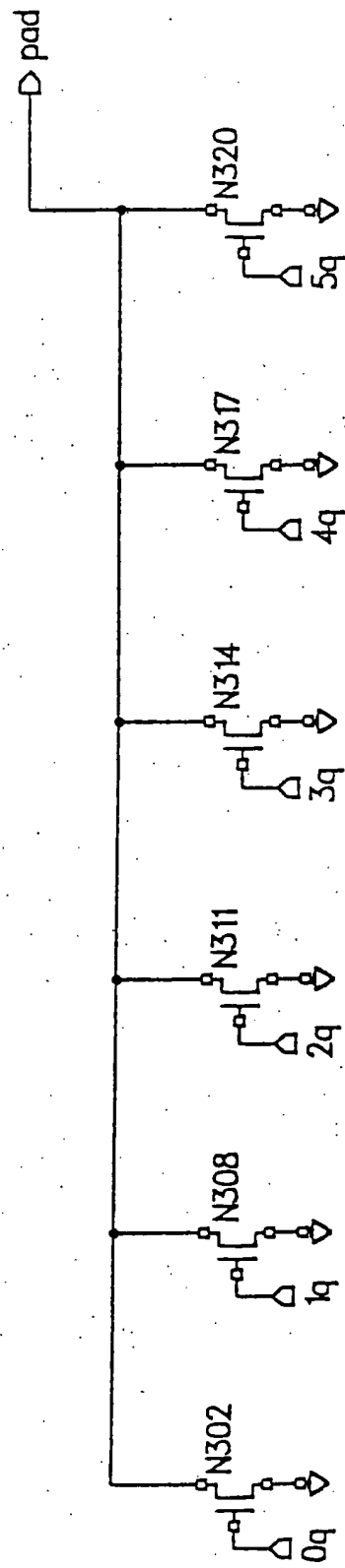


FIG. 4

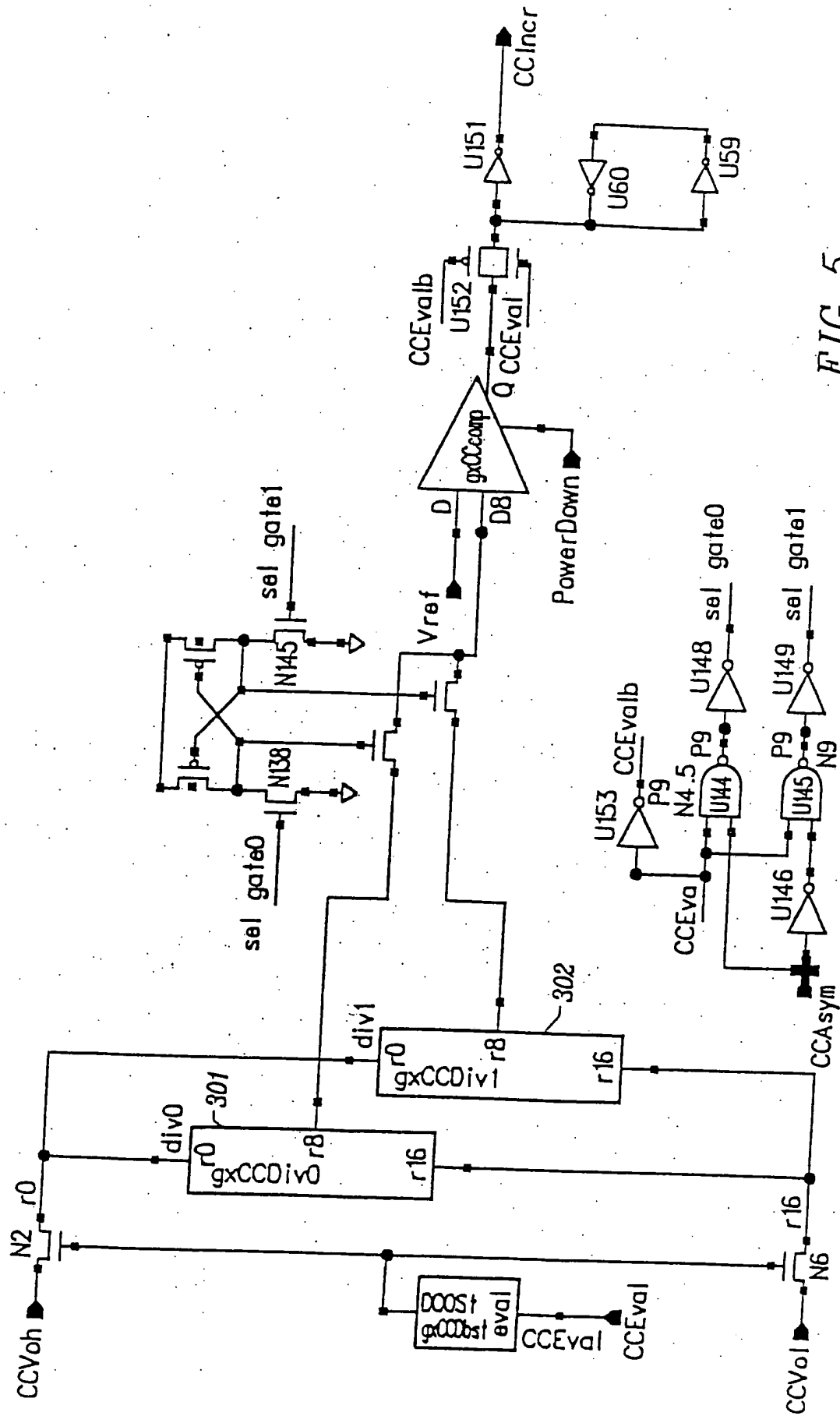


FIG. 5

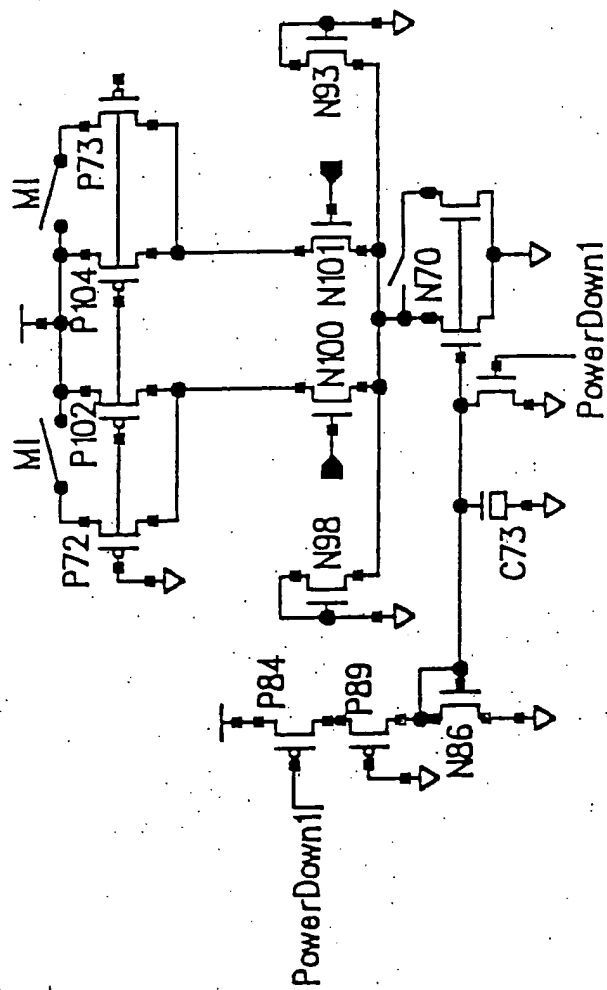


FIG. 7A

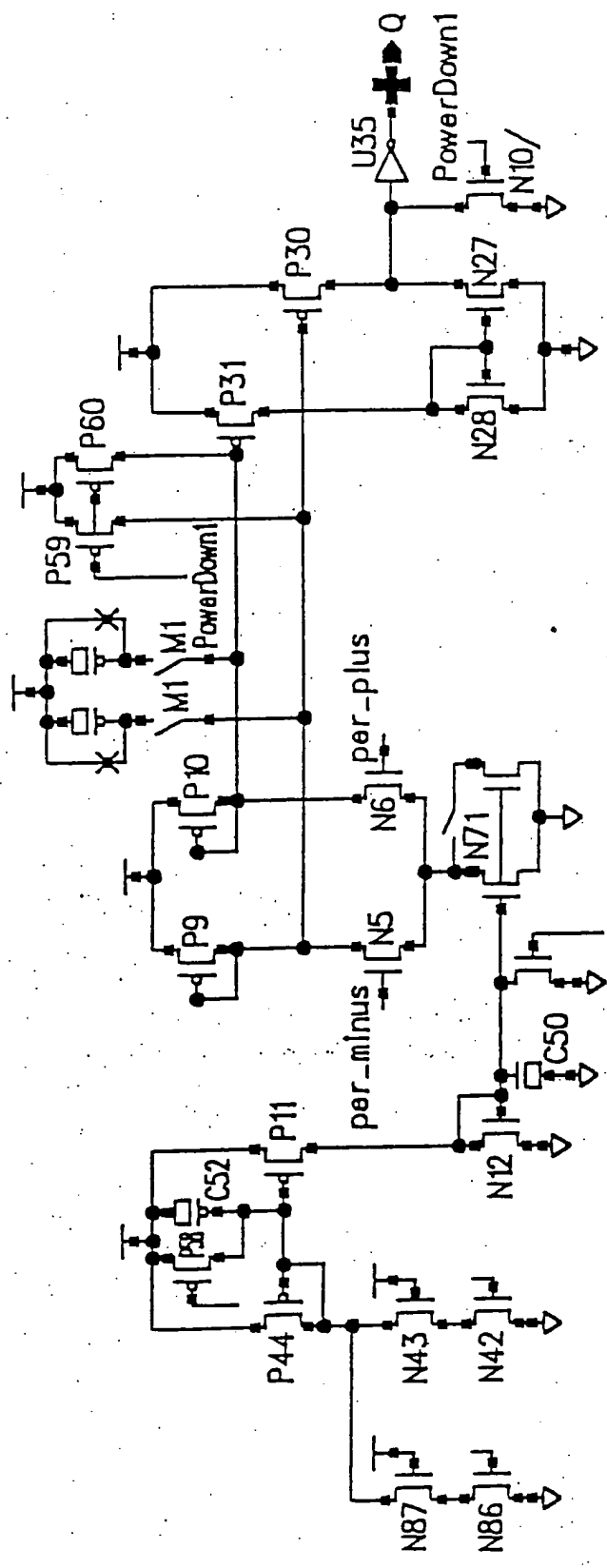


FIG. 7B

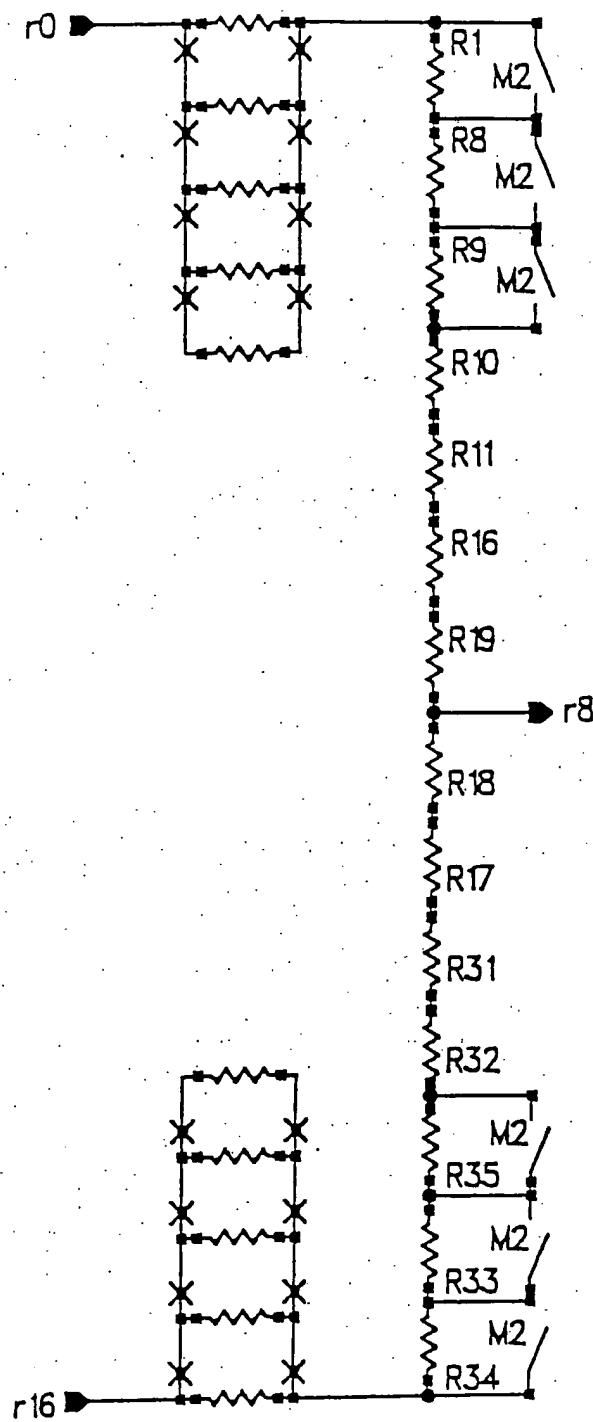


FIG. 8

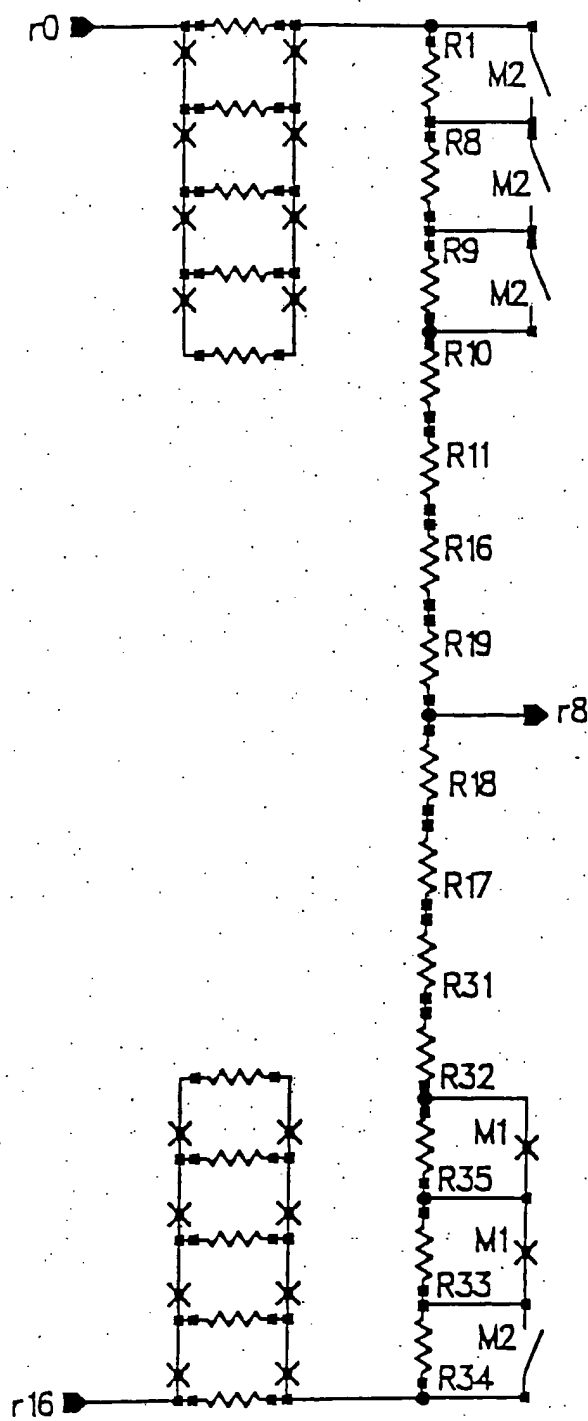
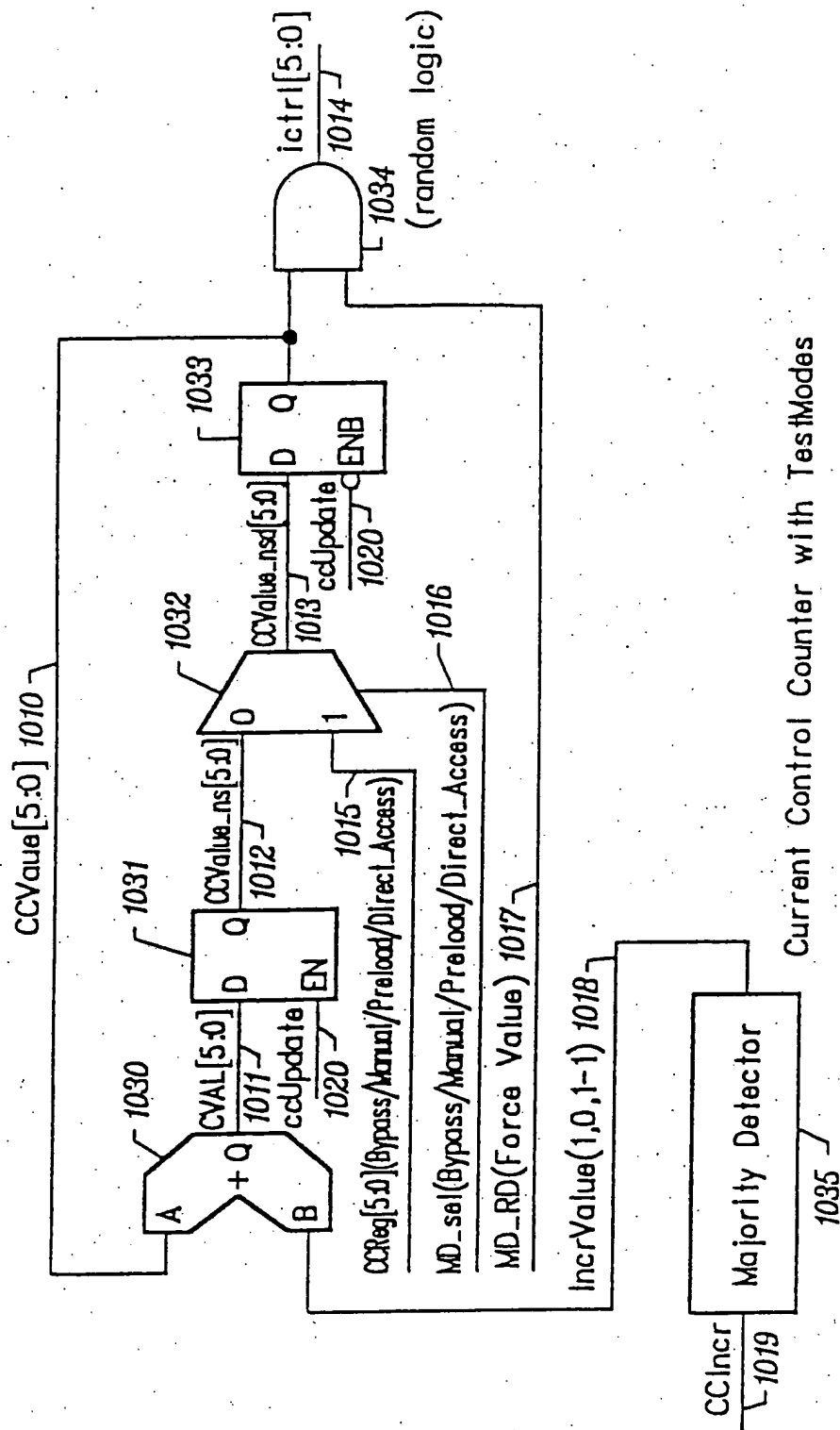


FIG. 9



Current Control Counter with TestModes

FIG. 10

FIG. 11
Current Control Logic Diagram

